

## Application Data Sheet

### Application Information

Application Type:: Regular  
Subject Matter:: Utility  
Title:: NONVOLATILE SEMICONDUCTOR MEMORY HAVING PLURAL DATA STORAGE PORTIONS FOR A BIT LINE CONNECTED TO MEMORY CELLS  
Attorney Docket Number:: 001701.00140  
Request for Early Publication?:: NO  
Request for Non-Publication?:: NO  
Total Drawing Sheets:: 70  
Small Entity?:: NO  
Secrecy Order in Parent Appl.?:: NO

### Applicant Information

Applicant Authority Type:: Inventor  
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Status:: Full Capacity  
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## **Correspondence Information**

Correspondence Customer Number:: 22907

## **Representative Information**

Representative Customer Number:: 22907

## **Domestic Priority Information**

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	Divisional	09/667,610	9/22/00

→ Now 6,373,746 B1

## **Foreign Priority Information**

Country::	Application number::	Filing Date::	Priority Claimed::
Japan	11-275327	9/28/99	Yes
Japan	11-345299	12/3/99	Yes

## **Assignee Information**

Assignee name:: Kabushiki Kaisha Toshiba

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